

REMARKS

Claims 1-5 are pending in the present application with all claims being rejected in the present Advisory Action.

Claims 1-5 were rejected based upon U.S. Patent No. 6,268,847 (Glen), U.S. Patent No. 5,844,623 (Iwamura), and U.S. Patent No. 5,844,541 (Cahill). In response Claims 3 and 5 were amended as per Examiner's suggestion.

In the Office Action mailed on October 6, 2003, the Examiner agreed that neither Glen nor Iwamura disclose "generating a timing signal for alternatively obtaining access to the first and second memories" recited in Claims 1 and 3 of the present invention. However, the Examiner suggests that Cahill makes that disclosure in, in column 28, lines 46-49 of its specification. The pertinent Cahill section states:

"There is preferably one buffer content indicator for each buffer used. In a preferred embodiment, two buffers are used in an alternating fashion, where one buffer is read by host processor 302 while the other buffer is filled by pixel processor 406."

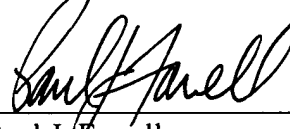
While such disclosure states that two buffers can be used alternatively. Cahill does not teach or describe "generating a timing signal for alternatively obtaining access to the first and second memories and providing the generated timing signal to the first and second memories" recited in Claims 1 and 4 of the present invention. Furthermore, neither Glen nor Iwamura disclose this step.

With regard to the Examiner's suggestion that providing a timing signal to the two memories would have been obvious, it must be pointed out that the present invention teaches not only providing the timing signal to the two memories but rather "generating a timing signal for alternatively obtaining access to the first and second memories and providing the generated timing signal to the first and second memories" recited in Claims 1 and 4. Such use of the timing signal is not obvious and is not described or taught by Glen, Iwamura, Cahill, or any combination thereof.

Without conceding the patentability per se of dependent Claims 2, 3, and 5, it is submitted that they overcome the prior art by virtue of their dependencies on independent Claims 1 and 4. Accordingly, it is submitted that Claims 1-5 are patentable.

In view of the above remarks and amendments, reconsideration and allowance of Claims 1-5 is respectfully requested. Applicant submits that pending Claims 1-5 are believed to be in condition for allowance and allowance is respectfully requested. Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicant's attorney at the number given below.

Respectfully submitted,



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